

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10039637	FILING DATE 12/31/2001	CLASS 316 200	SUBCLASS 10	GAU 2424	EXAMINER Bowers
**APPLICANTS: Srinivasan Vinoo; Nagbhushan Veerapaneni; Lalgudi Kumar;					
**CONTINUING DATA VERIFIED:					
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** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials				42390.P12454	
TITLE : Method and apparatus for layout synthesis of regular structures using relative placement					
<small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-436 (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
			Total Claims	Print Claim for 0.0	
ISSUE FEE			DRAWING		
Amount Due	Date Paid	Primary Examiner	Sheets Drawn	Figs. Drawn	Print Figs.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner		
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